

FIG. 1

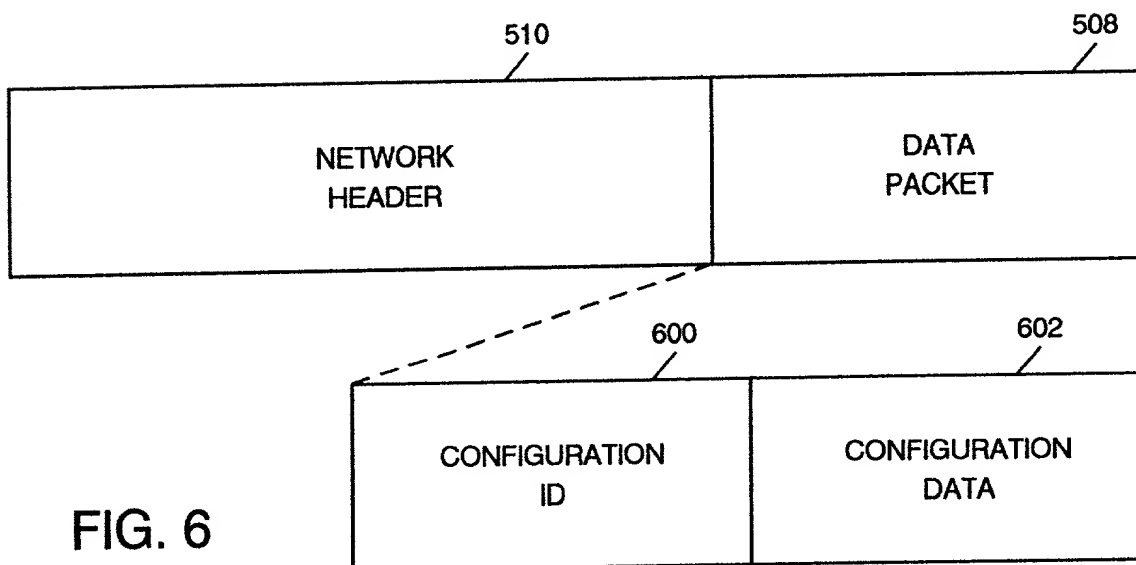
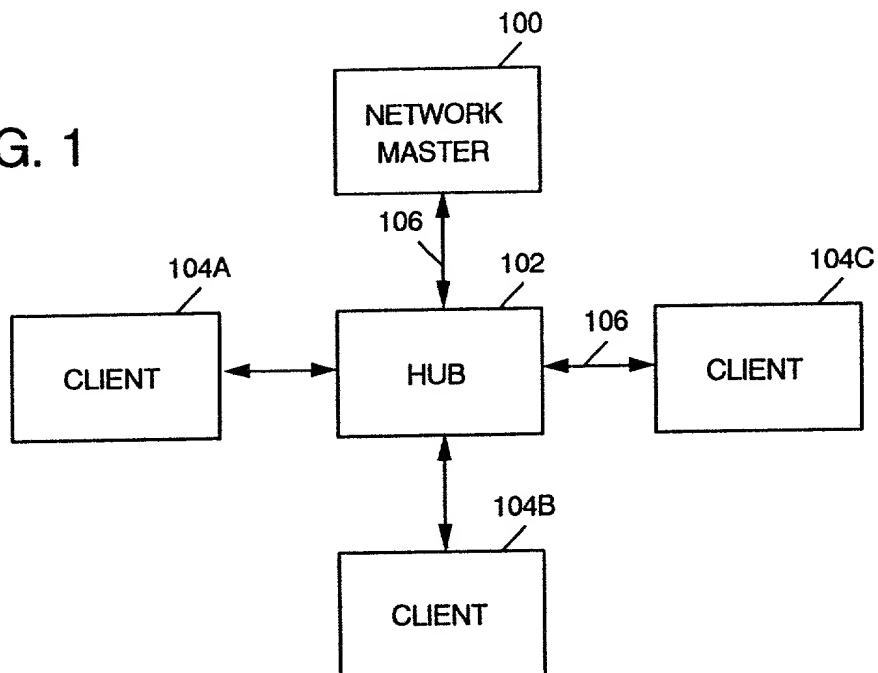
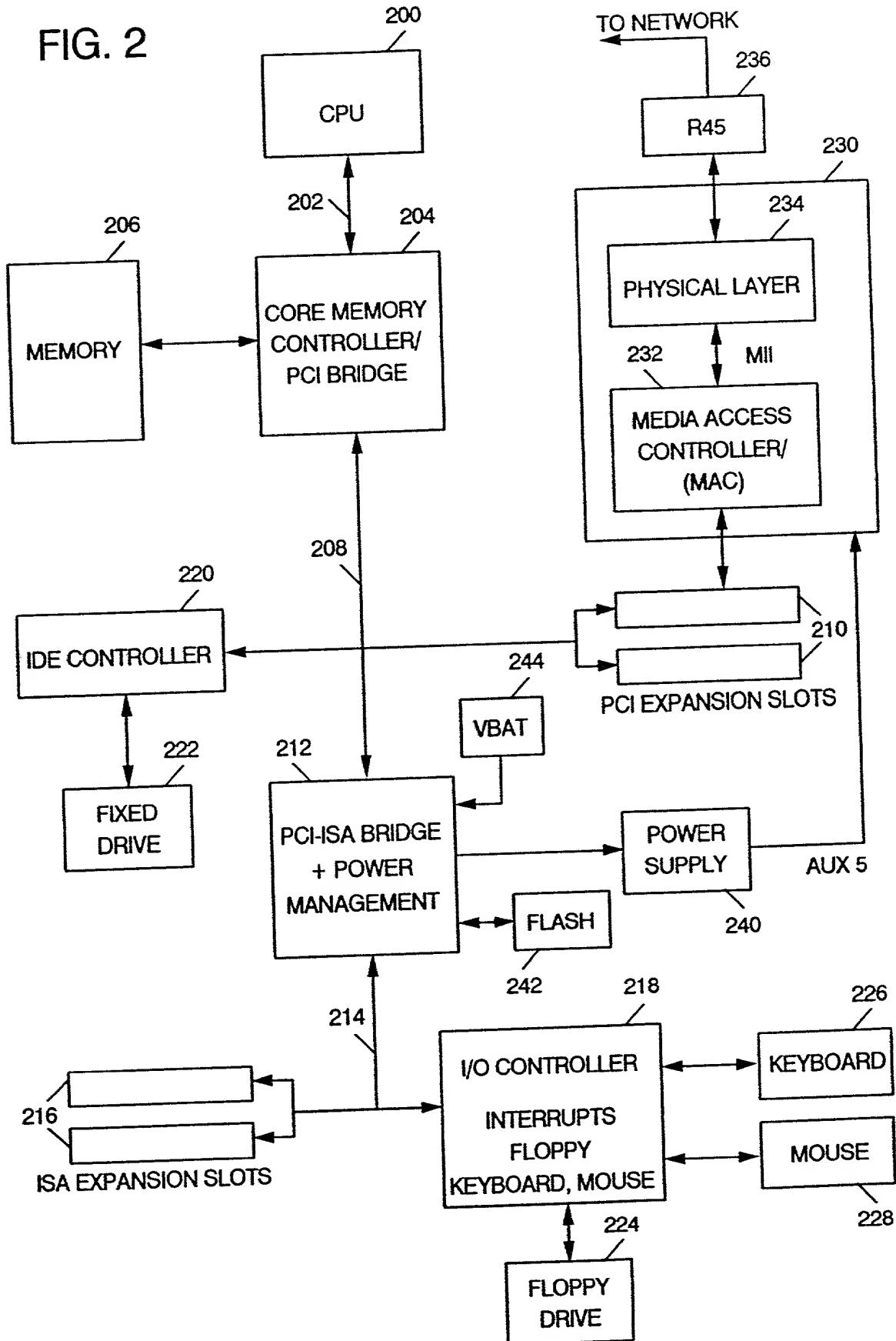


FIG. 6

OLD SERVER IP - 4 BYTES  
OLD SERVER MAC - 6 BYTES  
OLD UDP PORT - 2 BYTES

SYSTEM KEY - 16 BYTES  
GROUP KEY - 16 BYTES  
NEW SERVER IP - 4 BYTES  
NEW SERVER MAC - 6 BYTES  
NEW UDP PORT - 2 BYTES

FIG. 2



**FIG. 3**

The diagram illustrates a system architecture with the following components and connections:

- CPU (200)**: Connected to the **CORE MEMORY CONTROLLER/PCI BRIDGE (204)** via a bidirectional bus (202).
- MEMORY (206)**: Connected to the **CORE MEMORY CONTROLLER/PCI BRIDGE (204)** via a bidirectional bus (204).
- CORE MEMORY CONTROLLER/PCI BRIDGE (204)**: Acts as a central hub, connecting to the **IDE CONTROLLER (220)**, **PCH/ISA BRIDGE + POWER MANAGEMENT (212)**, and the **MGMT ASIC (300)**.
- IDE CONTROLLER (220)**: Connected to the **FIXED DRIVE (222)** via a bidirectional bus (222).
- PCH/ISA BRIDGE + POWER MANAGEMENT (212)**: Connected to the **IDE CONTROLLER (220)**, **MGMT ASIC (300)**, **VBAT (244)**, **FLASH (242)**, and **ISA EXPANSION SLOTS (216)**.
- MGMT ASIC (300)**: Part of a larger block (300) that also includes the **PHYSICAL LAYER (234)** and **MEDIA ACCESS CONTROLLER/(MAC) (232)**. It is connected to the **PHYSICAL LAYER (234)** via a bidirectional bus (234) and to the **MEDIA ACCESS CONTROLLER/(MAC) (232)** via a bidirectional bus (232).
- PHYSICAL LAYER (234)**: Connected to the **TO NETWORK** via a bidirectional bus (236).
- MEDIA ACCESS CONTROLLER/(MAC) (232)**: Connected to the **PHYSICAL LAYER (234)** and the **PCI EXPANSION SLOTS (210)** via a bidirectional bus (232).
- EEPROM (302)**: Connected to the **MGMT ASIC (300)** via a bidirectional bus (302).
- PCI EXPANSION SLOTS (210)**: Connected to the **MEDIA ACCESS CONTROLLER/(MAC) (232)** via a bidirectional bus (210).
- POWER SUPPLY (240)**: Connected to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** and the **MGMT ASIC (300)** via a bidirectional bus (240).
- FLASH (242)**: Connected to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** via a bidirectional bus (242).
- I/O CONTROLLER (218)**: Connected to the **PCH/ISA BRIDGE + POWER MANAGEMENT (212)** via a bidirectional bus (214) and to the **KEYBOARD (226)**, **MOUSE (228)**, and **FLOPPY DRIVE (224)** via a bidirectional bus (218).
- KEYBOARD (226)**: Connected to the **I/O CONTROLLER (218)** via a bidirectional bus (226).
- MOUSE (228)**: Connected to the **I/O CONTROLLER (218)** via a bidirectional bus (228).
- FLOPPY DRIVE (224)**: Connected to the **I/O CONTROLLER (218)** via a bidirectional bus (224).

FIG. 4

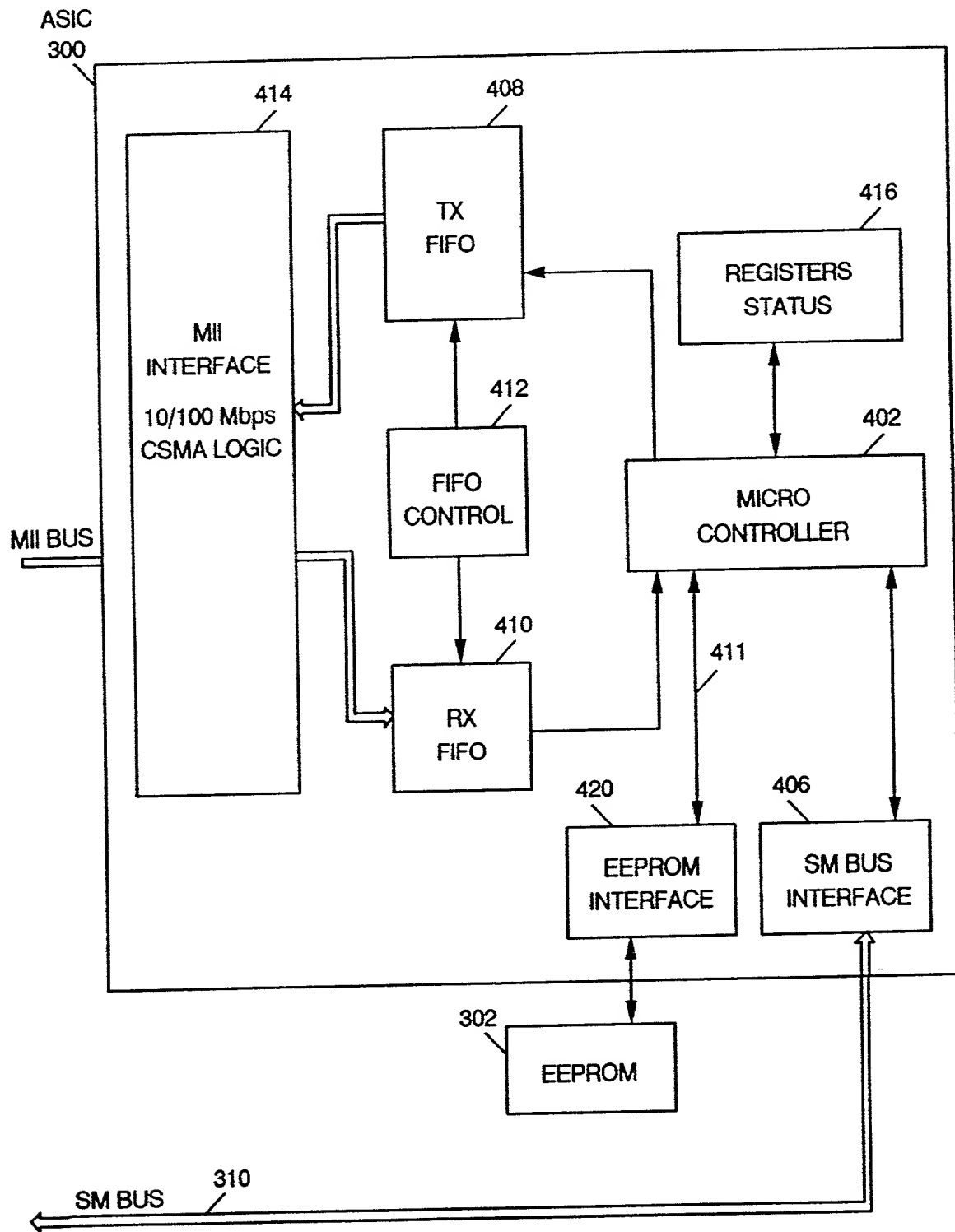


FIG. 5

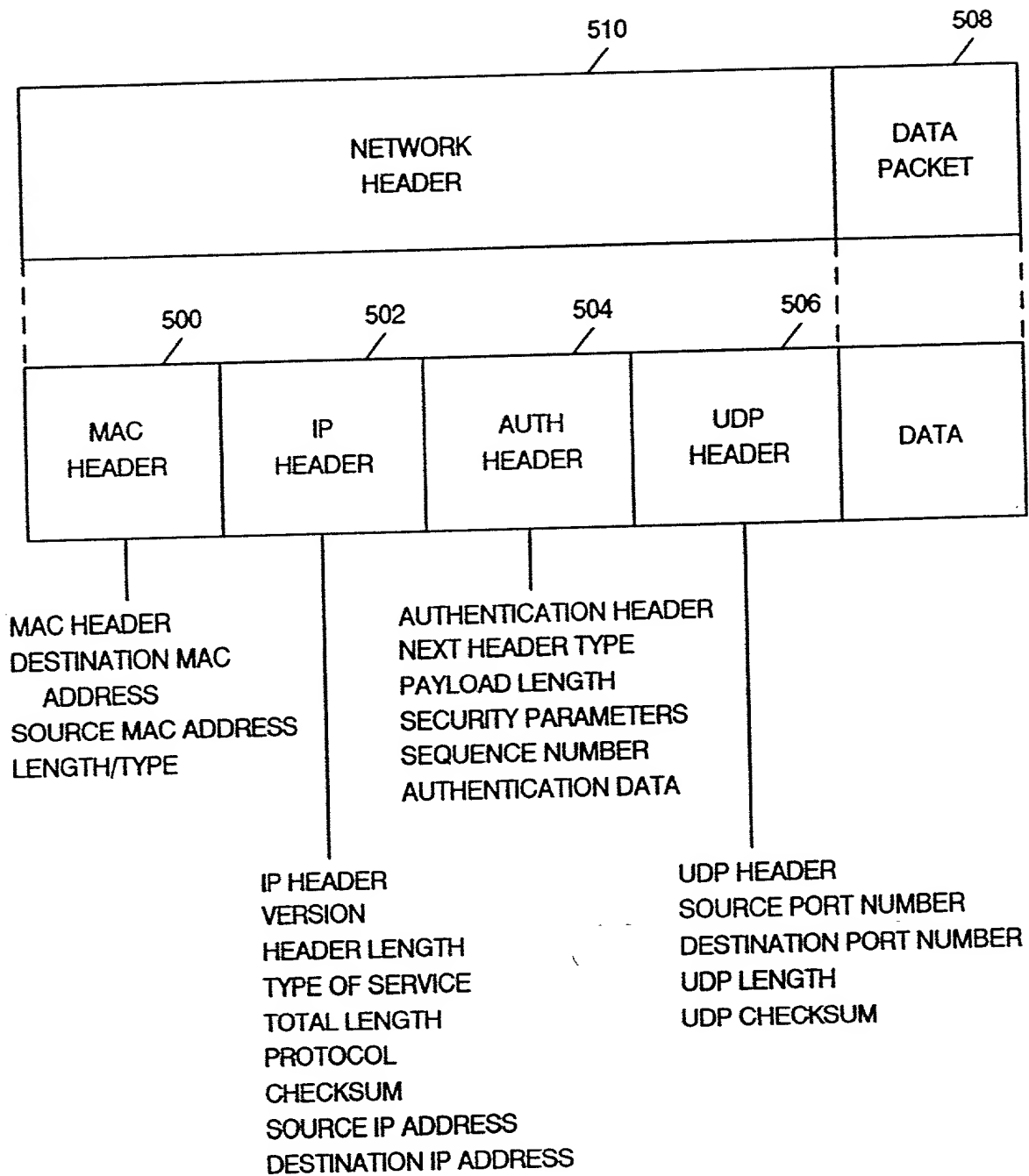


FIG. 7

